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Office of Initial Patent Examination's
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Re: U.S. Patent Application
Title: DOUBLE-GATED TRANSISTOR CIRCUIT
Serial No.: 10/719,119
Filed: November 21, 2003
Inventor(s): Douglas R. Hackler, Sr. and Stephen A. Parke
Our File No.: 51889/3

Gentlemen:

Enclosed is an official filing receipt. We have noted the following error:

1. The "Domestic Priority data as claimed by applicant" is missing. It should indicate the following information: 10/613,169 July 3, 2003.

Please issue a corrected filing receipt rectifying this error. Thank you for your assistance in this matter.

Very truly yours,

John R. Thompson

JRT/jlw
Enclosure

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/719,119	11/21/2003	2814	482	51889/3	14	26	4

John R. Thompson
 STOEL RIVES LLP
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 201 South Main Street, Suite 1100
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CONFIRMATION NO. 1799

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1 00000000011948354
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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Douglas R. Hackler SR., Boise, ID;
 Stephen A. Parke, Nampa, ID;

Domestic Priority data as claimed by applicant

10/613,169 July 3, 2003

Foreign Applications

If Required, Foreign Filing License Granted: 02/23/2004

Projected Publication Date: 05/26/2005

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

Double-gated transistor circuit

Preliminary Class

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